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BUFFERING METHOD AND APPARATUS FOR PROCESSING DIGITAL COMMUNICATION SIGNALS

ABSTRACT OF THE DISCLOSURE

A buffering apparatus to process digital communication signals includes a plurality of buffers, a processing unit, and programmed memory. The programmed memory has instructions directing the processing unit to process the digital samples corresponding to a group of symbols to be processed in a plurality of buffers. The digital samples start in a first buffer of the plurality of buffers and end in a second buffer of the plurality of buffers. The digital samples are received at a third buffer of the plurality of buffers during the processing of the digital samples.